Freeform Search

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Term:
l5 and l4 and l6 and l7 and l8 and l9 and l10

Display: 50 **Documents in Display Format:** - **Starting with Number** 1

Generate: ○ Hit List ● Hit Count ○ Side by Side ○ Image

[Search] [Clear] [Interrupt]

Search History

DATE: Tuesday, September 14, 2004   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
side by side			result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

Set Name	Query	Hit Count	Set Name
L13	l5 and l4 and l6 and l7 and l8 and l9 and l10	30	L13
L12	l5 and l4 and l6 and l7 and l8 and l9 and l10	30	L12
L11	l5 same l4 same l6 same l7 same l8 same l9 same l10	0	L11
L10	mos	771058	L10
L9	threshold$3	531357	L9
L8	modulat$3	657755	L8
L7	transistor$1	718033	L7
L6	((constant adj current) adj sourc$1)	28241	L6
L5	feedback or (fed adj back) or (feed$3 adj back)	490195	L5
L4	(compensat$3 adj circuit)	31328	L4
L3	L2 same modulat$3	18	L3
L2	L1 same (feedback or (feed$3 adj back) or (fed adj back))	334	L2
L1	transistor$1 with compensat$3 with current with source	2462	L1

END OF SEARCH HISTORY

h   e b   b g ee e   e   b   f ff e ch

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Term:
```
124 and (compensat$ adj circuit)
```

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[Search] [Clear] [Interrupt]

Search History

DATE: Tuesday, September 14, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		
L25	124 and (compensat$ adj circuit)	11	L25
L24	L22 and l21	63	L24
L23	L22 and l21	63	L23
L22	341/$.ccls. or 326/$.ccls. or 327/$.ccls.	154329	L22
L21	L20 and (l5 with l6) and l3 and l7	185	L21
L20	l10 same l1	2237	L20
L19	L18 same l4 same mos	0	L19
L18	l1 same l7 same l5 same l6 same l2 same l3	54	L18
L17	(L1 same l7) and (l5 with l6) and l2 and l3	1603	L17
L16	L15 and l1	650	L16
L15	(l5 with l6) and l10 and l2 and l3 and l7	1038	L15
L14	l11 and l7	3	L14
L13	l11 and l7	3	L13
L12	L11 same l7	0	L12
L11	L10 same l1 same (l5 with l6) same l2 same l3	4	L11
L10	(mos with l4)	90249	L10

L9	L8 same mos	0	L9
L8	l1 same l2 same l3 same l4 same l5 same l6 same l7	30	L8
L7	feedback or (feed$3 adj back) or (fed adj back)	490195	L7
L6	sourc$1	2973119	L6
L5	current	2688718	L5
L4	transistor$1	718033	L4
L3	modulat$3	657755	L3
L2	threshold$3	531357	L2
L1	compensat$	884673	L1

END OF SEARCH HISTORY

h   e b   b   g e e e   e   f ff e ch